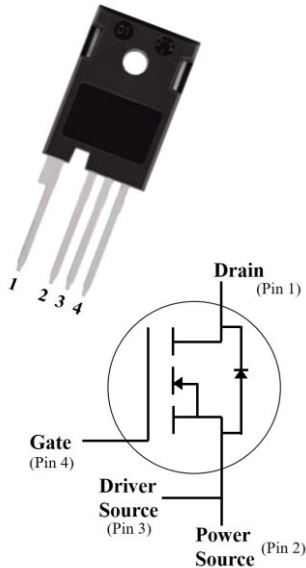


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	650V
I_D (25°C)	37A
$R_{DS(on)}$	60mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO247-4L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D206560NCFG1	
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	650	$V_{GS}=0\text{ V}, I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-8/+19	Absolute maximum values	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-4/+15	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	37	$V_{GS}=15\text{V}, T_c=25^\circ\text{C}$	Fig.18
Continuous drain current @ $T_c=100^\circ\text{C}$			28	$V_{GS}=15\text{V}, T_c=100^\circ\text{C}$	
Pulsed drain current	$I_{D(pulsed)}$	A	99	Pulse width t_p limited by $T_{j,max}$	
Power Dissipation	P_{TOT}	W	153	$T_c=25^\circ\text{C}, T_j = 175^\circ\text{C}$	Fig.17
Power Dissipation			66	$T_c=110^\circ\text{C}, T_j = 175^\circ\text{C}$	
Operating junction and Storage temperature range	T_j, T_{stg}	$^\circ\text{C}$	-55 to +175		
Soldering temperature	T_L	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	0.6	M3 screw Maximum of mounting process: 3	



■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	2.0	2.4	3.1	$V_{DS}=V_{GS}, I_D=5mA$	Fig.4, 11
				2.0		$V_{DS}=V_{GS}, I_D=5mA, T_j=175^\circ C$	
Drain source breakdown voltage	$V_{(BR)DSS}$	V	650			$V_{GS}=0, I_D=100\mu A$	
Zero gate voltage drain current	I_{DSS}	μA		1	20	$V_{DS}=650V, V_{GS}=0V$	Fig.16
Gate source leakage current	I_{GSS}	nA			250	$V_{GS}=15V, V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	m Ω		60	79	$V_{GS}=15V, I_D=13.2A$	Fig.5, 6, 7
				90		$V_{GS}=15V, I_D=13.2A, T_j=175^\circ C$	
Internal gate resistance	R_g	Ω		3		$f=1MHz$	
Diode forward voltage	V_{SD}	V		5.0		$V_{GS}=-4V, I_{SD}=6.6A$	Fig.8
				2.8		$V_{GS}=0V, I_{SD}=6.6A, T_j=175^\circ C$	Fig.9
Transconductance	g_r	S		10		$V_{DS}=20V, I_D=13.2A$	Fig.4
				9		$V_{DS}=20V, I_D=13.2A, T_j=175^\circ C$	

■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		1125		$V_{DS}=400V, V_{GS}=0V, T_j=25^\circ C, f=100kHz, V_{AC}=25mV$	Fig.13, 14
Output capacitance	C_{oss}			71			
Reverse capacitance	C_{rss}			7			
Coss stored energy	E_{oss}	μJ		7			Fig.15
Gate source charge	Q_{gs}	nC		13		$V_{DS}=400V, V_{GS}=-4/15V, I_D=13.2A$	Fig.12
Gate drain charge	Q_{gd}			17			
Gate charge	Q_g			46			

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on switching energy	E_{on}	μJ		67		$V_{DD}=400V, V_{GS}=-4/+15V, I_D=13.2A, R_g=2.5\Omega, L=135\mu H$	Fig.21, 22
Turn off switching energy	E_{off}			6			
Turn on delay time	$t_{d(on)}$	ns		8			
Rise time	t_r			11			



Turn off delay time	$t_{d(off)}$	ns		17	$V_{DD}=400V, V_{GS}= -4/+15V,$ $I_D=13.2A, R_g=2.5\Omega, L=135\mu H$	Fig.21, 22
Fall time	t_f			5		

■ **Body diode characteristics** ($T_c=25^\circ C$ unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V_{SD}	V		5.0		$V_{GS}=-4V, I_{SD}=6.6A$	Fig.8
				2.8		$V_{GS}=0V, I_{SD}=6.6A$ $T_j=175^\circ C$	Fig.9
Continuous diode forward current	I_S	A		24		$V_{GS}=-4V, T_c=25^\circ C$	Note1
Reverse recovery time	t_{rr}	nS		16		$V_R=400V, V_{GS}=-4V, I_D=13.2A, di/dt=$ $2400A/\mu S$	
Reverse recovery charge	Q_{rr}	nC		110			
Peak reverse recovery current	I_{rrm}	A		12			

Note 1: When using SiC Body Diode the maximum recommended $V_{GS} = -4V$

Note 2: MOSFET can also safely operate at 0/15 V

■ **Thermal Characteristics** ($T_a=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Typ.
Thermal resistance	$R_{\theta J-C}$	$^\circ C/W$	0.98

■ **Typical Characteristics**

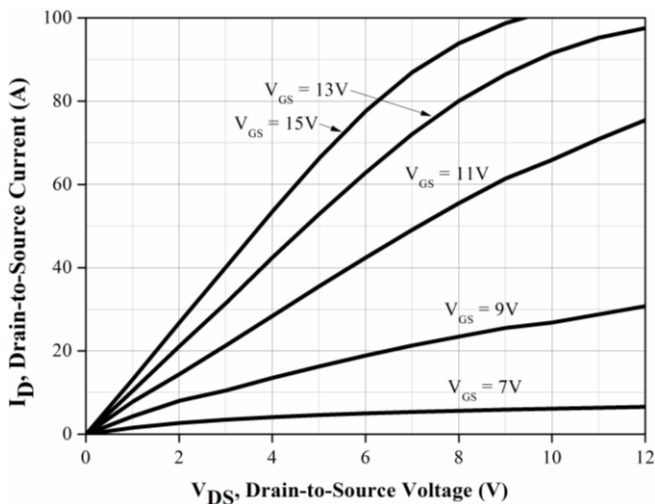


Figure 1. Output Characteristics $T_j = -55^\circ C$

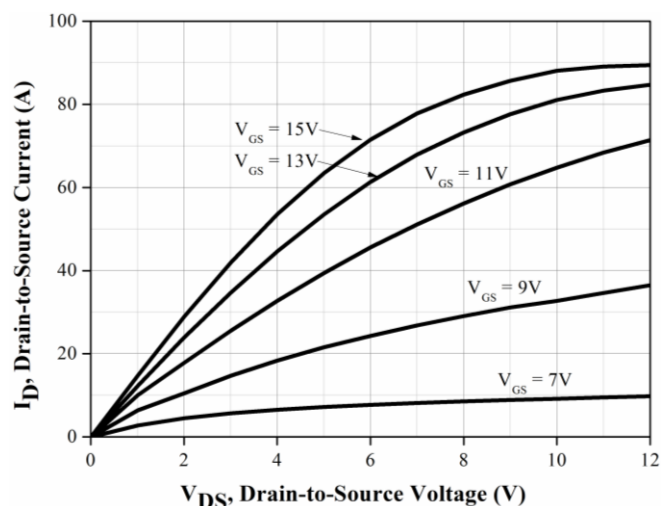


Figure2. Output Characteristics $T_j = 25^\circ C$

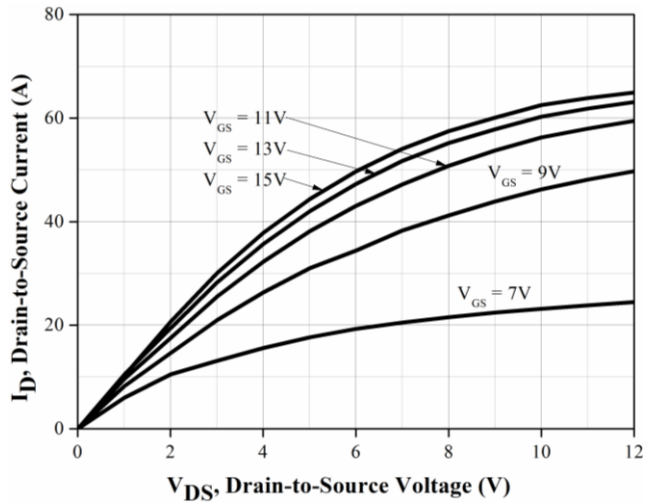


Figure 3. Output Characteristics $T_j = 175^\circ\text{C}$

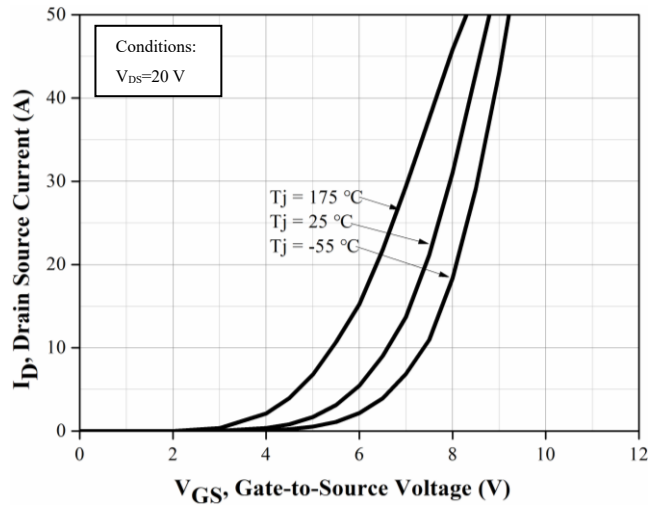


Figure 4. Transfer Characteristics for various junction temperature

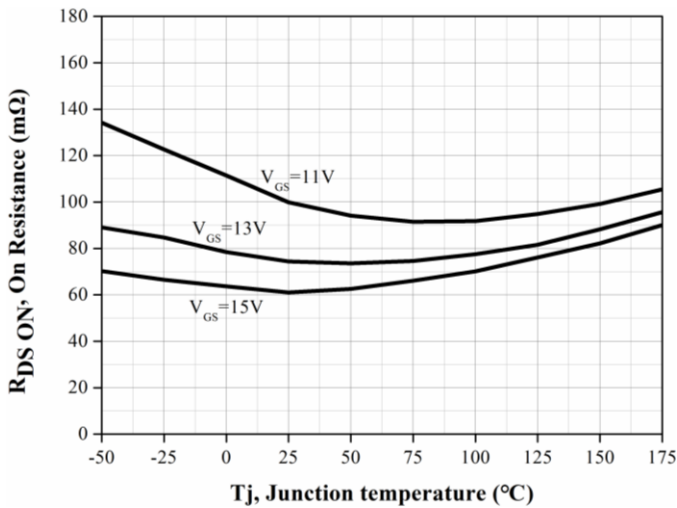


Figure 5. On-resistance vs. temperature for various gate voltage

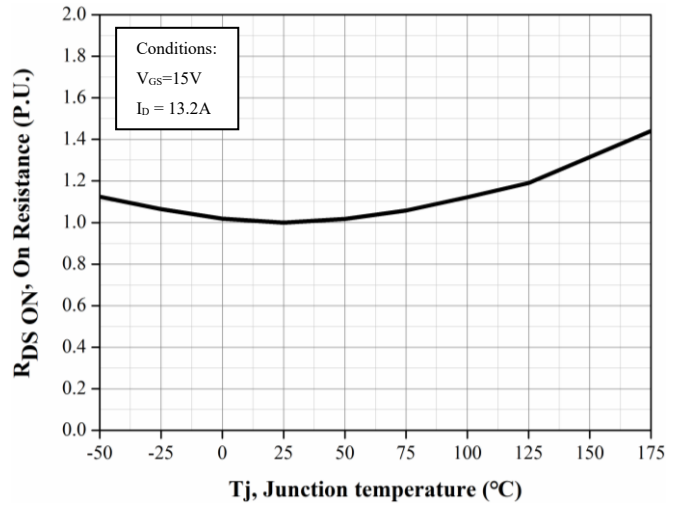


Figure 6. Normalized on-resistance vs. temperature

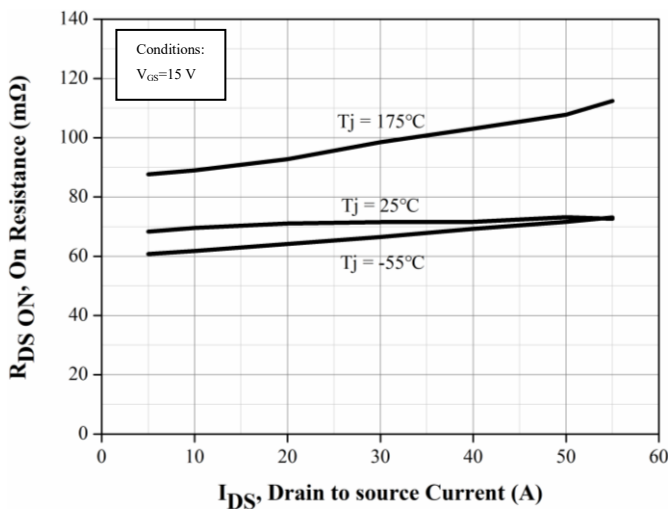


Figure 7. On-resistance vs. drain current

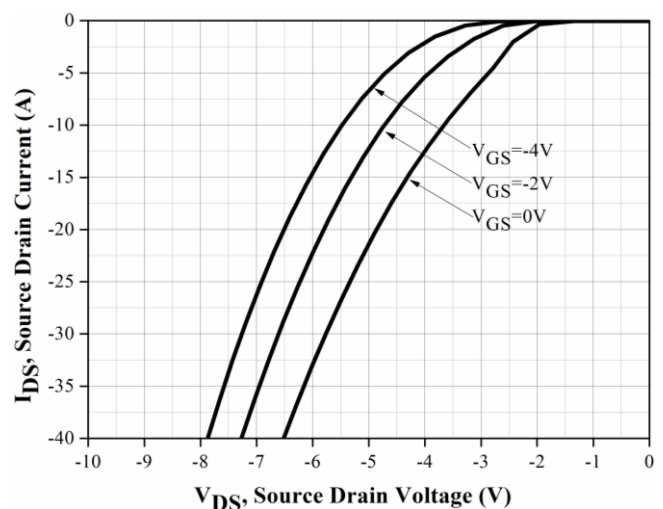


Figure 8. Body diode characteristic at $T_j = 25^\circ\text{C}$

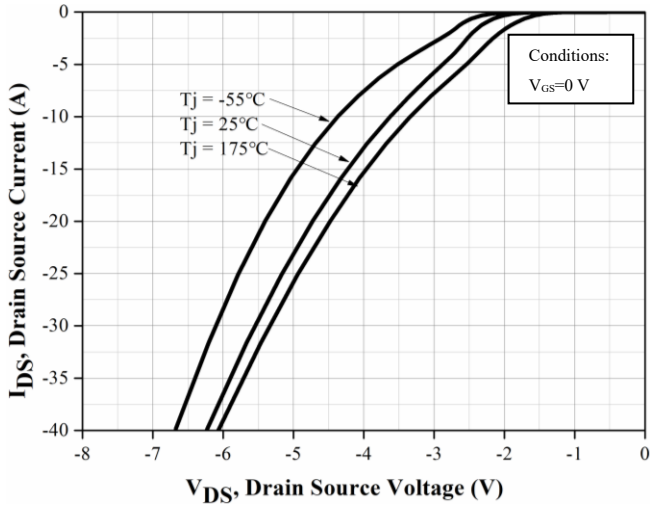


Figure 9. Body diode characteristic

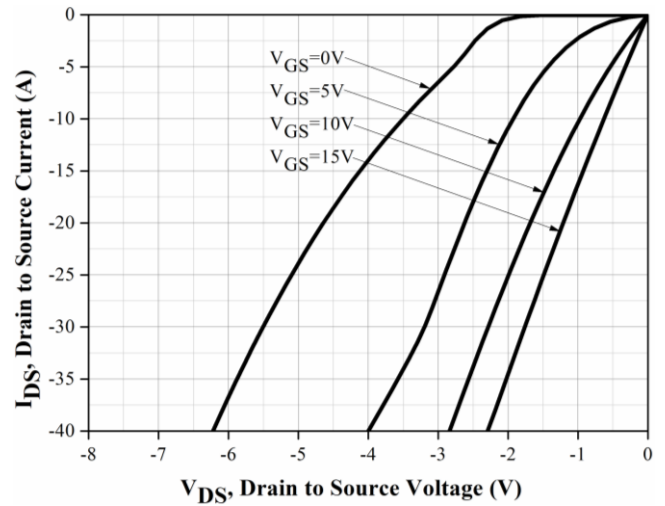


Figure 10. 3rd quadrant characteristic at $T_j = 25^\circ\text{C}$

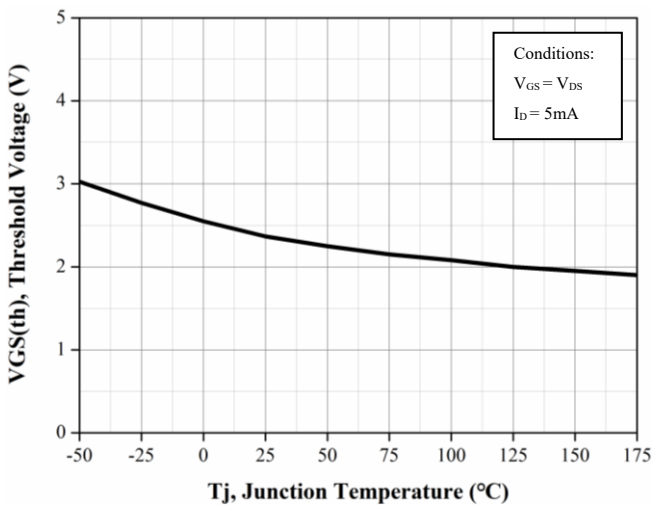


Figure 11. Threshold voltage vs. temperature

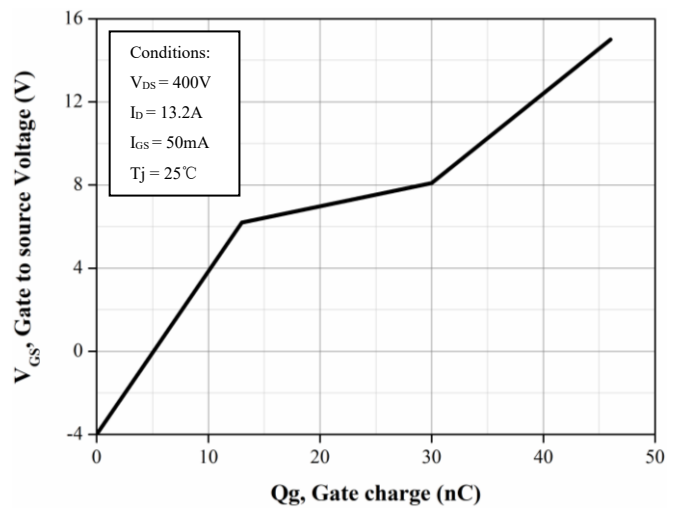


Figure 12. Gate charge characteristic

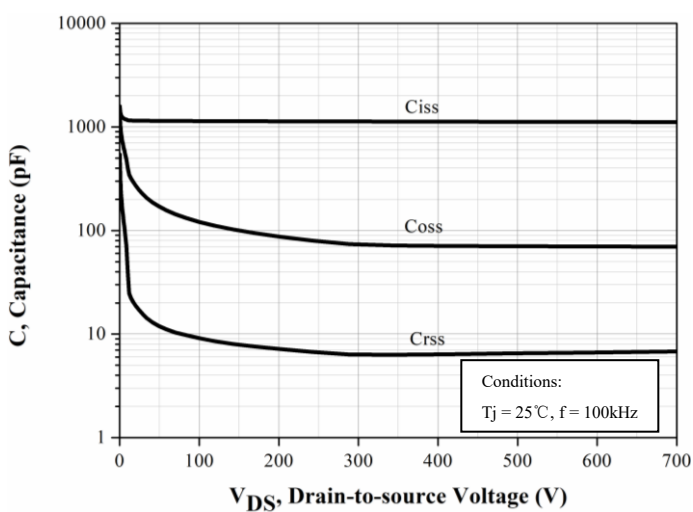


Figure 13. Capacitances vs. drain source voltage (0-700V)

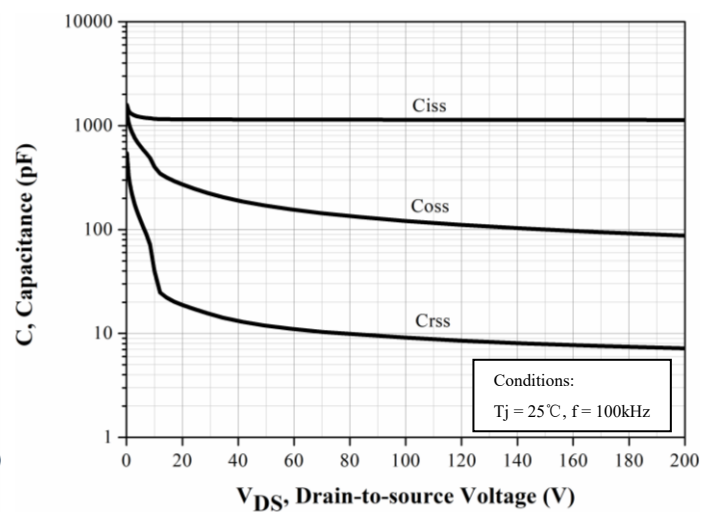


Figure 14. Capacitances vs. drain source voltage (0-200V)

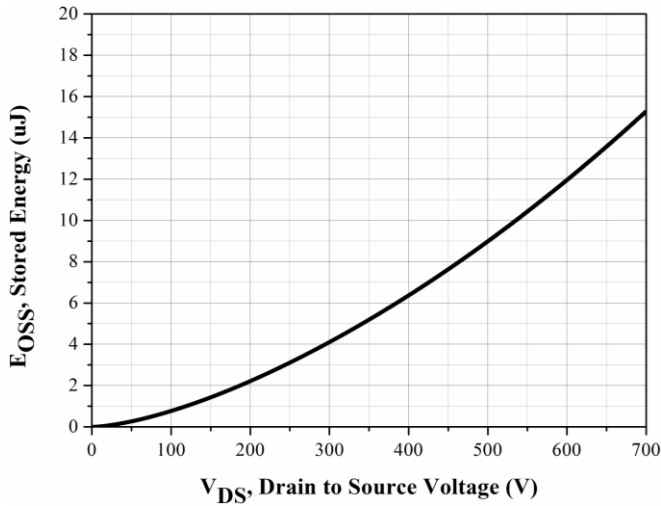


Figure 15. Output capacitor stored energy

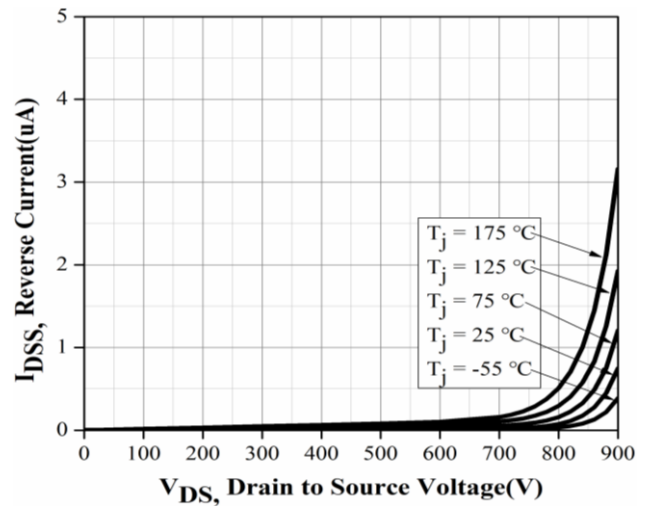


Figure 16. Reverse characteristics vs. Tj

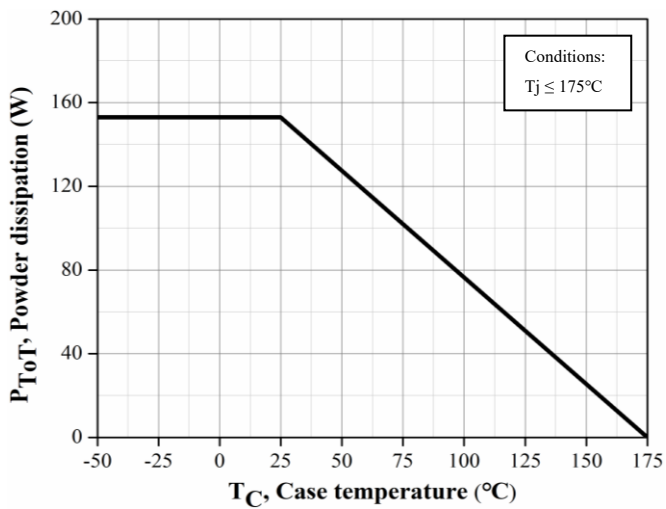


Figure 17. Maximum power dissipation derating vs. case temperature

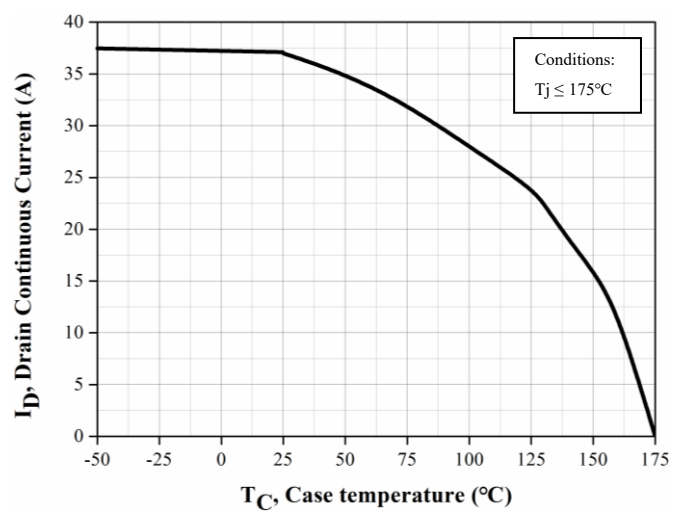


Figure 18. Continuous drain current derating vs. case temperature

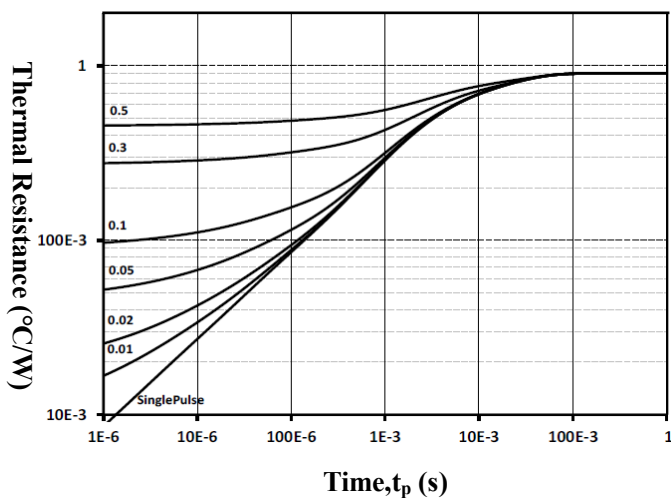


Figure 19. Transient thermal impedance (junction - case)

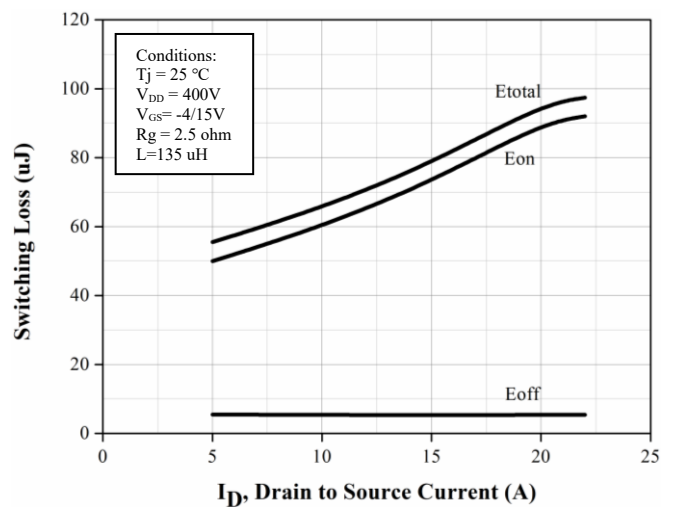


Figure 20. Clamped Inductive switching energy vs. drain current

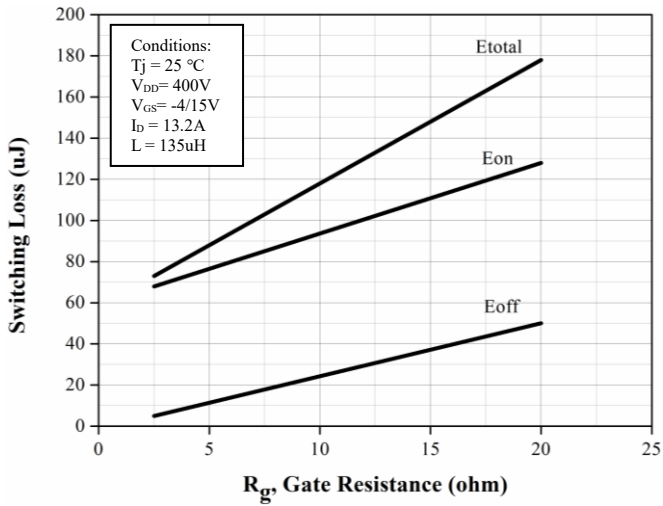


Figure 21. Clamped inductive switching energy vs. R_g

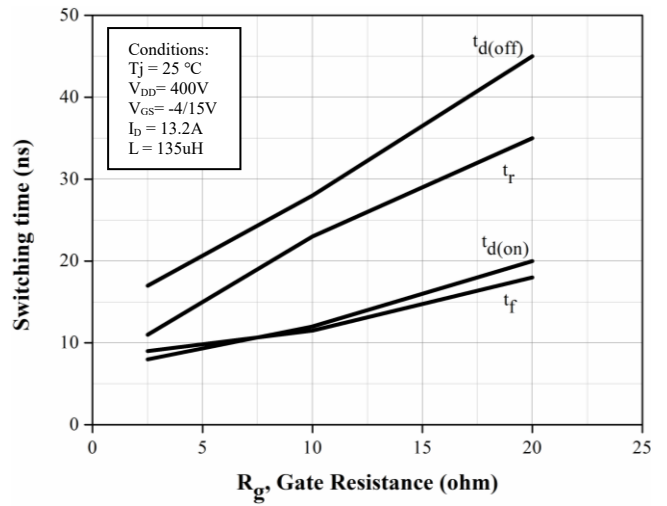


Figure 22. Switching times vs. R_g

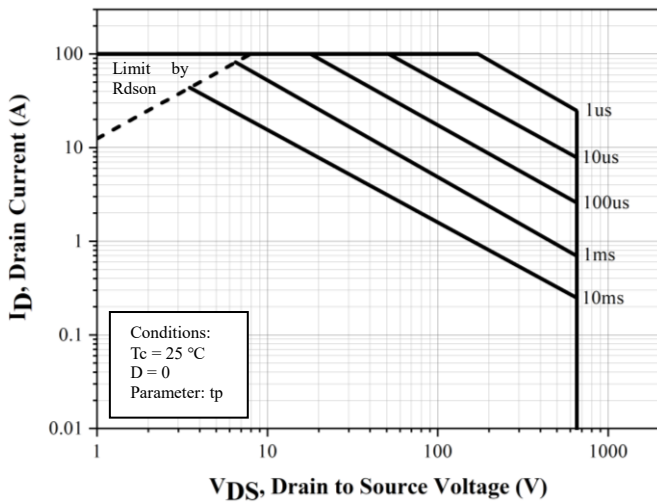


Figure 23. Safe Operating Area

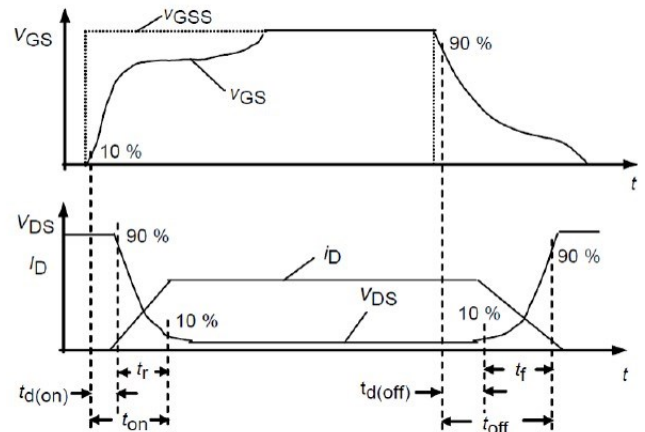


Figure 24. Switching Times Definition

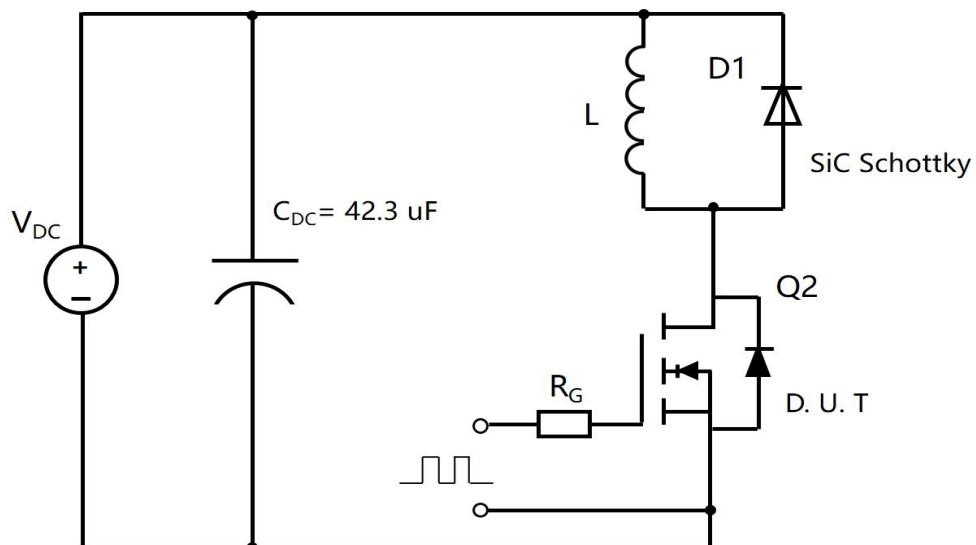
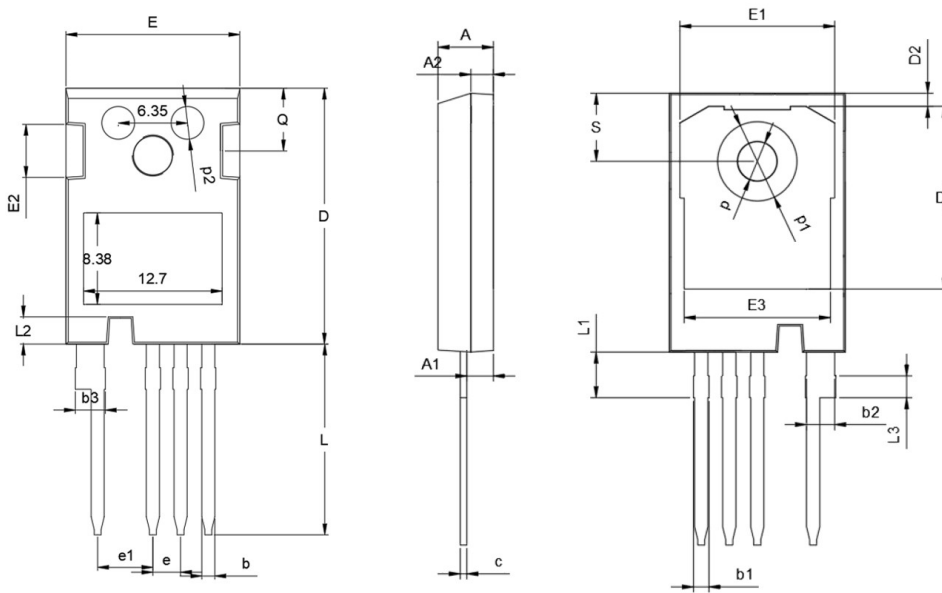


Figure 25. Clamped Inductive Switching Waveform Test Circuit

■Outline Dimensions



TO247-4L			
Dim	Min	Norm	Max
A	4.80	5.00	5.20
A1	2.30	2.40	2.50
A2	1.88	1.98	2.08
b	1.10	1.20	1.30
b1	1.20	/	1.50
b2	2.35	2.55	2.75
b3	2.45	/	2.85
c	0.55	0.60	0.65
D	23.3	23.45	23.6
D1	16.25	16.55	16.85
D2	1.00	/	1.30
e	TYP2.54		
e1	TYP5.06		
E	15.75	15.90	16.05
E1	13.80	/	14.20
E2	4.40	4.75	5.10
E3	13.00	/	13.45
L	17.34	17.49	17.64
L1	4.00	/	4.30
L2	2.35	/	2.65
L3	TYP1.98		
Q	5.60	5.80	6.00
S	6.05	/	6.30
p	TYP3.58		
p1	TYP7.18		
p2	TYP3.00		



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